EELE 203. Circuits II for Engineering. 4 Credits. (3 Lec, 1 Lab) F.S.Su
PREREQUISITE: EELE 201, M 274. Natural and forced response of R-L-C
circuits, frequency response of R-L-C circuits and Bode plots, frequency response,
slew-rate and DC imperfections of real op-amps; Laplace Transform, Fourier series
and Fourier Transform techniques in circuit analysis; basic R-L-C and op-amp
filters; two port networks.

Term  CRN  Section  Session/Dates  Days  Location  Time
2018 Summer 10724 001  May Start  MTWR  ROBH208  9:00am - 11:00am
2018 Summer 10724 001  May Start  MTWR  ROBH208  4:00pm - 5:00pm
2018 Summer 10725 002  May Start  MTWR  COBH601  1:00pm - 3:00pm

EELE 250. Circuits, Devices and Motors. 4 Credits. (3 Lec, 1 Lab) F.S
PREREQUISITE: M 166Q or M 172Q and PHSX 207 or PHSX 222. Introduction
for non-majors to electrical circuit principles, voltage and current laws, frequency
response; introduction to electronic circuits including operational amplifiers, and
power electronics; introduction to electromechanical energy conversion devices,
DC and AC machines.

Term  CRN  Section  Session/Dates  Days  Location  Time
2018 Summer 11619 001  May Start  MTWR  ROBH312A  8:00am - 9:00am
2018 Summer 11619 001  May Start  MTWR  ROBH312A  10:00am - 11:00am
2018 Summer 11619 001  May Start  MTWR  ROBH312A  12:00pm - 1:00pm
2018 Summer 11620 002  May Start  MTWR  COBH601  3:10pm - 5:10pm

EELE 261. Intro To Logic Circuits. 4 Credits. (3 Lec, 1 Lab) F.S
An introductory course in the fundamental concepts of classical digital design.
Course covers design and implementation of combinational logic circuits,
synchronous sequential circuits and information storage circuits. Basic concepts of
Hardware Description Languages(HDLs), design and simulation of digital systems
using HDLs, and digital system implementation with programmable logic devices
are presented.

Term  CRN  Section  Session/Dates  Days  Location  Time
2018 Summer 11248 801  June-start: 4x4  -  -  -

EELE 367. Logic Design. 4 Credits. (3 Lec, 1 Lab) S
PREREQUISITE: EELE 261 Advanced combinational and sequential logic design.
Laboratory experience implementing advanced logic designs using FPGAs.

Term  CRN  Section  Session/Dates  Days  Location  Time
2018 Summer 11691 801  July-start: 4x4  -  -  -

EELE 591. Special Topics. 1-4 Credits. (1-4 Lab; 12 cr max)
-- Special Topics.

Term  CRN  Section  Session/Dates  Days  Location  Time
2018 Summer 11479 801  Intersession  -  -  -
2018 Summer 11211 001  Non-standard  -  -  -
term dates 14-
MAY-18 03-
AUG-18
Font Notice

This document should contain certain fonts with restrictive licenses. For this draft, substitutions were made using less legally restrictive fonts. Specifically:

Times was used instead of Adobe Garamond Pro.

The editor may contact Leepfrog for a draft with the correct fonts in place.