EELE - Electrical Engineering

EELE 203. Circuits II for Engineering. 4 Credits. (3 Lec, 1 Lab) S
PREREQUISITE: EELE 201, M 274. Natural and forced response of R-L-C circuits, frequency response of R-L-C circuits and Bode plots, frequency response, slew-rate and DC imperfections of real op-amps; Laplace Transform, Fourier series and Fourier Transform techniques in circuit analysis; basic R-L-C and op-amp filters; two port networks.

Term | CRN  | Section | Session/Dates | Days | Location | Time
--- | --- | --- | --- | --- | --- | ---
2017 Summer | 10724 | 001 | First Half | MTWR | ROBH319 | 9:00am - 11:00am
Session | | | Session | |
2017 Summer | 10725 | 002 | First Half | F | COBH602 | 9:00am - 12:00pm
Session | | | Session | |

EELE 261. Intro To Logic Circuits. 4 Credits. (3 Lec, 1 Lab) F,S
An introductory course in the fundamental concepts of classical digital design. Course covers design and implementation of combinational logic circuits, synchronous sequential circuits and information storage circuits. Basic concepts of Hardware Description Languages (HDLs), design and simulation of digital systems using HDLs, and digital system implementation with programmable logic devices are presented.

Term | CRN  | Section | Session/Dates | Days | Location | Time
--- | --- | --- | --- | --- | --- | ---
2017 Summer | 11248 | 801 | Full Semester | - | - | -
Session | | | |

EELE 591. Special Topics. 1-4 Credits. (1-4 Lab; 12 cr max)
--- Special Topics.

Term | CRN  | Section | Session/Dates | Days | Location | Time
--- | --- | --- | --- | --- | --- | ---
2017 Summer | 11211 | 001 | Non-standard term dates 10-1-17 to 14-7-17 | - | - | -
Session | | | | | | |
Font Notice

This document should contain certain fonts with restrictive licenses. For this draft, substitutions were made using less legally restrictive fonts. Specifically:

Times was used instead of Adobe Garamond Pro.

The editor may contact Leepfrog for a draft with the correct fonts in place.