EELE 203, Circuits II for Engineering. 4 Credits. (3 Lec, 1 Lab) ES,Su
PREREQUISITE: EELE 201, M 274. Natural and forced response of R-L-C circuits, frequency response of R-L-C circuits and Bode plots, frequency response, slew-rate and DC imperfections of real op-amps; Laplace Transform, Fourier series and Fourier Transform techniques in circuit analysis; basic R-L-C and op-amp filters; two port networks.

Term  CRN  Section  Session/Dates  Days  Location  Time
2020 Summer  10512 001  May-start: 4x4  MTWR  -  9:00am - 11:00am
2020 Summer  10513 002  May-start: 4x4  MTWR COBLEI602  1:00pm - 4:00pm Semester

EELE 250, Circuits, Devices and Motors. 4 Credits. (3 Lec, 1 Lab) ES,Su
PREREQUISITE: M 166Q or M 172Q and PHSX 207 or PHSX 222. Introduction to electronic circuits including operational amplifiers, and power electronics; introduction to electromechanical energy conversion devices, DC and AC machines.

Term  CRN  Section  Session/Dates  Days  Location  Time
2020 Summer  11217 001  First Half  MTWR ROBERT210  8:00am - 8:50am Session
2020 Summer  11217 001  First Half  MTWR ROBERT210  10:00am - 10:50am Session
2020 Summer  11217 001  First Half  MTWR ROBERT210  12:00pm - 12:50pm Session
2020 Summer  11218 002  First Half  MTWR COBLEI620  2:00pm - 4:00pm Session

EELE 261, Intro To Logic Circuits. 4 Credits. (3 Lec, 1 Lab) ES,Su
An introductory course in the fundamental concepts of classical digital design. Course covers design and implementation of combinational logic circuits, synchronous sequential circuits and information storage circuits. Basic concepts of Hardware Description Languages (HDLs), design and simulation of digital systems using HDLs, and digital system implementation with programmable logic devices are presented.

Term  CRN  Section  Session/Dates  Days  Location  Time
2020 Summer  11155 802  June-start: 4x4  -  -  - Semester
2020 Summer  11156 803  First Half  -  -  - Semester
2020 Summer  11157 801  Full Semester  - ONLINEWEB- Semester
2020 Summer  10846 804  May-start: 4x4  -  -  - Semester

EELE 367, Logic Design. 4 Credits. (3 Lec, 1 Lab) SSu
PREREQUISITE: EELE 261 Advanced combinational and sequential logic design. Hardware descriptice language (HDL) programming knowledge. Laboratory experience implementing advanced logic designs using FPGAs.

Term  CRN  Section  Session/Dates  Days  Location  Time
2020 Summer  11076 803  May-start: 4x4  -  -  - Semester
2020 Summer  11158 802  June-start: 4x4  -  -  - Semester
2020 Summer  11159 801  Non-standard term dates 18- MAY-20 07- AUG-20 ONLINEWEB-
2020 Summer  11160 804  Second Half  -  -  - Semester
2020 Summer  11161 805  Full Semester  -  -  - Semester